

HF-DR series

High Stability RF Generator for Ion Traps and Quadrupole Structures

HF-DR_PaulDrives_Manual3_4.doc
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Datasheet Rev. 3.4

Model HF-DR

Main Features:

- RF drive 1MHz to 40MHz
- suited for Yb⁺, Ca⁺, Be⁺ and more (ion clocks, quantum computing)
- amplitudes up to 3000Vpp
- ppm-level amplitude stability available (option)

Purpose and Description of the Device

HF-DR series drives provide AC sine wave voltages to Paul Ion Traps and other Quadrupole-type electrode setups for ion storage and manipulation. Unlike standard RF (radiofrequency) generators and power amplifiers, the device is capable to handle significant capacitive loads, which are related to vacuum setups for ion trapping.

Importantly, the device can stabilize the output amplitude at a highly accurate level (up to 3ppm over one minute) using dedicated stabilization loops. This greatly eases applications like ion clocks or quantum computing ion traps.

Depending on the model variant, one or two outputs are installed, in the latter case they differ by 180°, thus effectively doubling the voltage seen by a trapped particle. The HF-DR device is designed to deliver up to several kiloVolts of amplitude (peak-peak) at frequencies typically between 1 MHz and 10 MHz into typically 20pF to 80pF load on each output (please request detailed specification), eventually up to 3000Vpp at lower frequencies, depending on device options. Customized versions around ~16MHz or ~40MHz are feasible as well.

This generator is housed in a standard 19-inch rack-mount case and features a remote control feature, by which the user can control the device via a standard USB connection.

Optionally, the device features a fast *turn-off* circuit, to release the stored particles quickly out of the trapping volume by damping the output resonator, within one oscillation period.

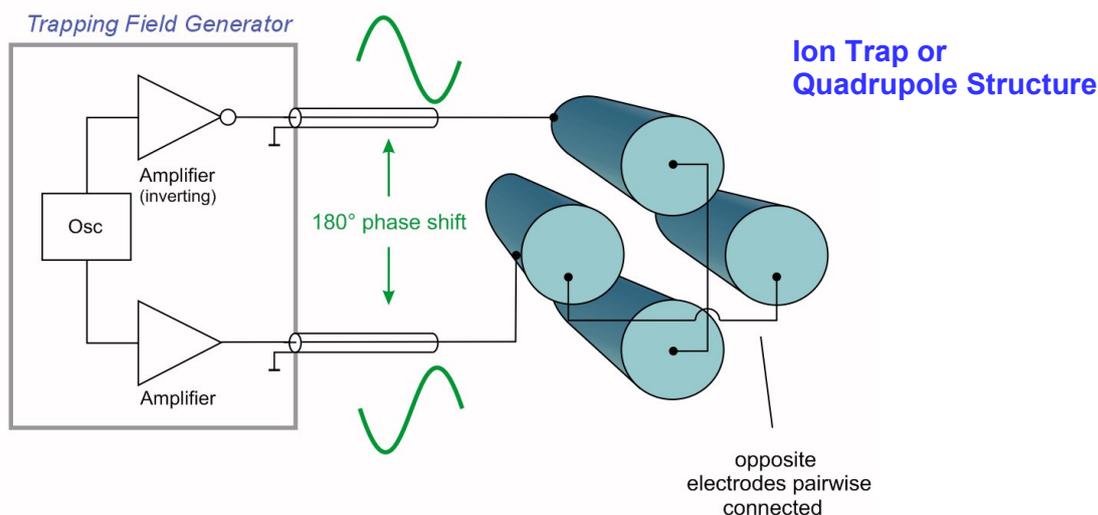


Fig. 1: Typical RF drive application: supply of Paul Traps or RF Ion Guides. The twin output doubles the effective voltage due to the 180° phase shift.

Device Nomenclature and Parameter Range

Example: HF-DR 3.5 - 900 FL

- └ Floating Coil option
- └ nominal output voltage (peak-peak, Volts) each channel, i.e. double value, 1800Vpp *between* the output channels
- └ nominal center frequency in MHz

The device is available as single channel version, dual channel (0°,180°) and dual channel with 'Floating coil' option 'FL' (0°,180° outputs). The (differential) amplitudes range between 500Vpp to 1800Vpp (max. 3000Vpp), depending on model, with center frequencies between 1.0MHz to 8MHz at typically 50pF load each channel (not all combinations are available, please contact manufacturer).

Functional Principle and Block Diagram

The following picture (fig. 2a) displays a block diagram of the internal structure, illustrating the functional principle. A digital oscillator provides a sine wave of freely adjustable and precise frequency. An inverter circuit creates the inverted signal, or in other words a 180° shift. These two signals are fed into power amplifiers of adjusted amplification factor, followed by a resonant circuit. The latter steps up the obtainable voltage (about 20Vpp out of the power amplifier, PA) roughly by a factor of 50 to 100. Thus, voltages of a few hundred volts to about 2kV (peak-peak values) are created, suited for Paul Ion Traps or Quadrupole Structures. For balancing possible asymmetries, trim capacitors are provided (rear side) to balance different capacitive output loads, thus obtaining equal output levels on both outputs. The device features an offset DC input, by which the output voltages can be shifted DC-related. Voltages being applied to these input lines are added to the AC signal (RF) of the outputs (this feature is not shown in fig. 2a/b). This also applies for multiple DC offset, e.g. for linear quadrupole or octupole structures.

Optionally a fast turn-off feature can be installed. This can swiftly damp away the energy in the output resonator, reducing the amplitude within less than one oscillation period close to zero.

The USB section allows for remote control of essential functions of the device via a standard USB connection. Please refer to control elements on front plate in next chapter and command syntax for remote control in the appendix.

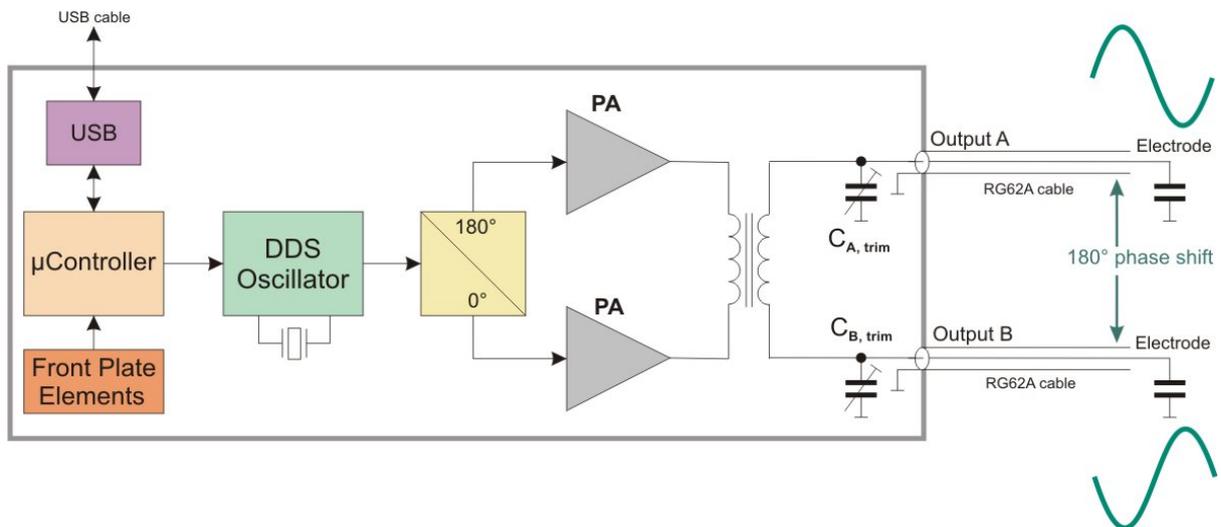


Fig. 2 : Block diagram of the internal structure, illustrating the functional principle. For simplicity, the amplitude regulation loops and fast-turn-off circuitry are not depicted.

Safety Hints

Risk of electrical shock	This device can produce high voltages at its output lines, which are harmful and dangerous in case of direct touch with the human body. Voltages may be even exceeded, in case that an additional external offset voltage is applied to the respective input. Care must be taken to avoid unintentional touching of any output line, and also if the device is brought into contact to any other device which might be endangered by high voltages. Operation of this device is admissible only for qualified personnel.
Read all installation, operation, and safety instructions	Prior to operation, thoroughly review all safety, installation, and operating instructions accompanying this equipment.
Rear side switch turns device completely off	If the device is not in use for a longer time, it is recommended to turn the mains switch at rear side off.
This equipment must be connected to an earth safety ground	This product is grounded through the grounding conductor of the power cord. To avoid electrical hazard, the grounding conductor must be connected to protective earth ground.
Do not modify the unit	Do not make electrical or mechanical modifications to this unit.
Change cabling only when device is off	Changing the cabling, when voltages are present at the outputs can lead to formation of harmful sparks.
Do not operate in wet/damp conditions	To avoid electric shock hazard, do not operate this product in wet or damp conditions. Protect the device from humidity and direct water contact.
Beware of external magnetic fields	External magnetic fields can impair, damage or even destroy this device. A maximum external field strength of no more than $B = 5\text{mT}$ is admissible. Having placed the device at any time into an external magnetic of bigger $B = 5\text{mT}$ (regardless, if power was turned on or off) can lead to severe overheating of the device and severely increased hazard of fire.
Service is to be performed by qualified service persons only	All servicing on this equipment must be carried out by factory-qualified service personnel only.
Do not block chassis ventilation openings	Slots and openings in the chassis are provided for ventilation purposes to prevent overheating of the equipment and must not be restricted. All case vents should continuously be cleared, in order to prevent overheating. If in doubt about the sufficiency of air ventilation, provide a software readout of the internal temperature sensor for regular inspection, e.g. every 2 minutes. A temperature over 55°C indicates inadequate air ventilation.
Routinely cleaning from dust	After long operation, or operation in a dusty environment it is strongly recommended to have the internal parts of the device cleaned by the manufacturer, or an appropriately qualified workshop in order to reduce the hazard of overheating.
No outdoor operation	Outdoor operation of the device is not admissible.

Operation and Control elements



Fig. 1: The front plate contains the main control elements of the device:

Mains Supply Switch

The device is powered up after activating the rear-side mains supply switch and switching the power button on the front plate into the “on” position. A Power-on-LED (green) indicates proper operation of the internal circuitry. A warning beeper will temporarily sound, which is used for ventilation fan-speed monitoring. If the warning beeper permanently sounds, the device must not be put into operation. In general, if the device is not in use for a longer time, it is recommended to deactivate the rear side mains switch to cut the device completely off from mains supply. This is mainly for safety reasons.

Control Keys

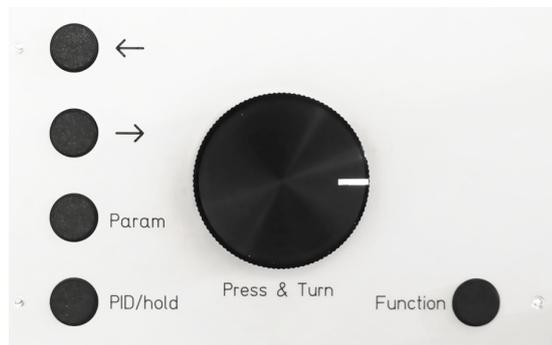


Fig. 2a: Control keys and steering wheel on front plate

The device is manually operated by the control keys and selection wheel. Pressing the button ‘Param’ chooses the parameter to be changed, i.e. **drive level** for outputs A and B and the **frequency**. Arrows → and ← choose the digit to be changed. ‘Function’ is reserved for special functions and routinely toggles the device from ‘manual’ to ‘remote mode’. In the latter the device is controlled via the USB bus interface by a PC, and manual entries are locked out. For ‘PID/Hold’ function see below.

Phase Output

The HF-DR devices feature an internal precision oscillator, which provides a logic level (i.e. 0V / 5V, rectangular signal) to monitor the oscillator’s phase. This output is helpful e.g. in case of an ion extraction out of the trap and exact timing with small jitter ($< 0.6\text{ns}_{\text{rms}}$) is required. External triggers or pulse generators may be synchronized to this output therefore. Optionally, this output is synchronized additionally to the BNC *input* for deactivating the output (see appendix).

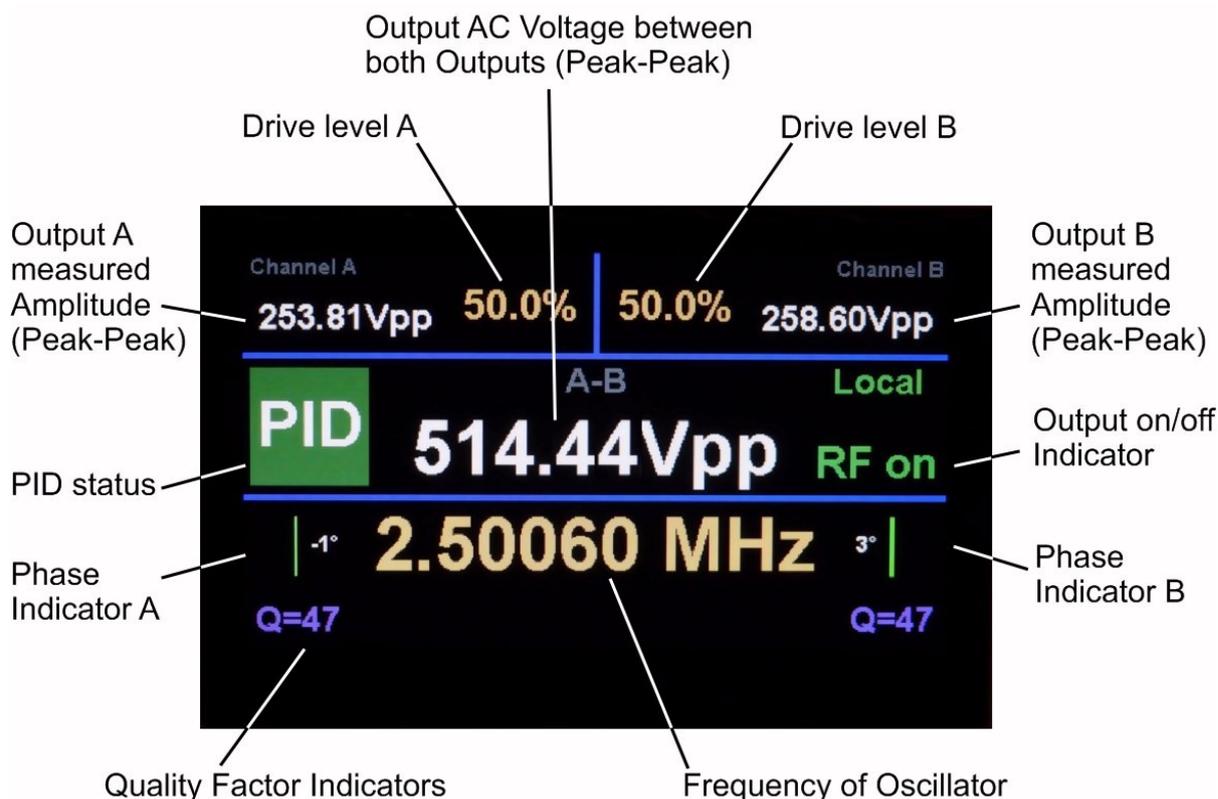


Fig. 2b: LCD Display on front side. Appearance may change upon device version.

LCD Display

The LCD Display shows the main functions and parameters of the device, like chosen drive level, selected frequency, PID status. The display reduces light intensity after 60 minutes to prolongate its lifetime. For general operation of the device see below (picture above shows display of the Dual-Channel version). The details of display contents may differ slightly on the model version.

Activate/Deactivate Switch

This input allows for activating and deactivating the output amplitude of the device. It may be operated

- manually with the switch on the front plate
- remotely via USB connection or
- using the logic level being applied to the BNC input socket (left open: activated)

A low level at the BNC input socket here deactivates the RF power, high level activates it. For safety reasons the manual switch position, being switched to ‘off’ overrides other settings.

In case the optionally available **fast-turn-off feature** is installed, the output waveform is terminated in very short time (see chapter below), e.g. for ion ejection out of an ion trap.

In general, a deactivation of the output signal should not happen at a too frequent repetition rate. A rate of no more than 5 Hz (i.e. cycling the output power 5 times per second) is recommended to maintain a long lifetime of the fast-turn-off switching unit. Note that optionally the fast-turn-off feature is synchronized to the internal oscillator phase to ensure the the turn-off switch is activated always at the same RF phase (see appendix).

Excitation Input

If this option is installed, the excitation input allows for adding a small AC voltage to one of the RF outputs (normally output A). This serves for creating an additional electrical field component e.g. for ion excitation at a frequency well below the trapping field frequency. The applied voltage is linearly superposed to the RF field. An amplitude up to 20Vpp may be applied here. The LED indicator on the front plate has a gradual onset above approx. 3.5Vpp voltage. The transfer function, i.e. the response of the output line upon applying a signal on the BNC input depends on the choice on the output resonator. The general diagram is depicted below, also a transfer plot for the example of 2.5MHz.

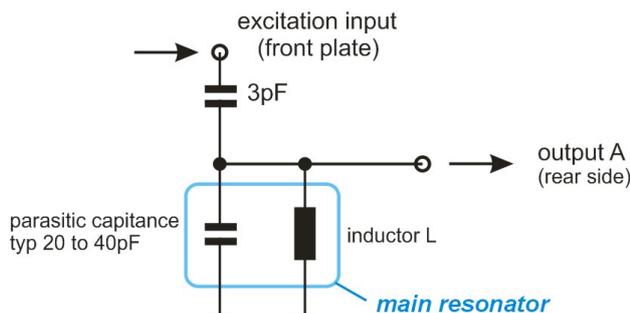


fig. 3a general diagram illustrating the circuitry of the excitation input. An AC signal is coupled via small capacitor to the output resonator. See table below for attenuation examples, indicating how much attenuated an input signal will appear at the output.

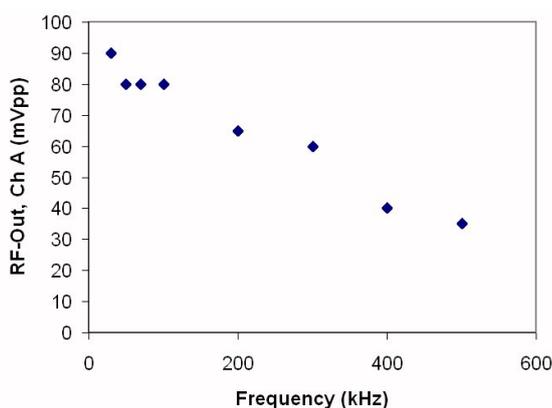


fig. 3b transfer function from excitation input to channel A for a 2.5 MHz resonator, 20Vpp being applied at front side excitation input.

Resonator frequency	Excitation frequency	Attenuation, typical value
1 MHz	50 kHz	1/300
''	100 kHz	1/150
2.5 MHz	50 kHz	1/650
''	100 kHz	1/330
6 MHz	50 kHz	1/1500
''	100 kHz	1/800

Table 1: Typical attenuation factors, from front side excitation input to rear side output. Note that the attenuation factor doubles in case of a single output line.

Basic Device Operation

As the device represents a *resonant* amplifier, it needs to be manually frequency-tuned to the specific experiment. Therefore, it is recommended to firstly complete the wiring to the Ion Trap (or Quadrupole Structure). Make sure there are no shortcuts and connect to the device using appropriate cabling (normally coaxial cables), which can sustain the voltages and which protects from unintentional touching. In case of an external resonator box (applies for frequencies above approx. 8MHz), connect the latter correspondingly.

In order to start operation, put the 'Activate/Deactivate Switch' to the 'enable' position (regarding output Option FL, see also below). The display should show 'RF on' now. Increase the drive level (by pressing the 'Param' button and turning the selection wheel) to about 50% for each channel. The amplitude indicators should start to show some amplitude value now. Press the 'Param' button to select the frequency settings (cursor blinking) and **optimize the observed amplitude to its maximum carefully by tuning the frequency**. One may select e.g. the 10kHz digit and move the selection wheel by hand carefully to the left or to the right (lower or higher frequency) to search for increasing output voltage reading. In case both partial amplitudes on outputs A and B are substantially different one may also use the trim capacitors at the rear side of the device. These trim capacitors are meant for balancing parasitic capacitances on the output lines, or connected Ion Trap trap respectively to obtain equal output levels on outputs A and B. Subsequently optimize *again* the oscillator frequency to obtain maximum amplitude and eventually trim again an eventual unbalance.

Note, that after increasing the amplitude drive level to high values (larger 60%) one may need to tune again the frequency settings after some while (15 minutes) to account for slow temperature drifts in the output resonator coil resonance.

In case a phase monitor is installed (this does not apply for the floating coil option 'FL') one can check whether the set frequency is too low or too high while looking at the phase indicators. They start to show a value once the amplitudes on the output channels are above 20Vpp approximately. If the indicated phase is below zero, decrease the frequency, otherwise increase it. Alternatively, the trim capacitors can be used to change the resonance frequency. Increasing the trim capacitor value (turn to 'max cap') will lower the resonance frequency, otherwise (turn towards 'min cap') will increase it. Note that the trim capacitor knobs on the rear side of the device have a 180° turning range, the trim range is approx. 40pF. Note that the trim capacitor knobs have no fixed endpoints, please check their labelling for their min./max. capacitance position, otherwise 'overturning' will result in unexpected results.

In case the output amplitudes become too high the device automatically set the drive levels back to lower values, this maximum amplitude may be customized, see the remote control / command list section for details. However, be careful while increasing the drive levels, in order to protect the device and also circuitry and the experimental setup.

It is strongly recommended not to operate the device higher than specified. Also, beware, not to operate the device for longer times (>30s) at high driver levels *out of resonance*, since overload/overtemperature conditions may result, which could be harmful for the device.

Note that a sudden drop of amplitude or phase change may indicate sparking (e.g. inside the attached vacuum setup/ion trap). In this case try to locate the problem immediately and avoid running the system close to the onset point of the problem to prevent damage by overheating or overvoltages.

Output Option FL

Most commonly, the output option FL is chosen, with a floating output coil (see fig. 2, which is the standard option in devices after production 12/2018) ensuring good symmetry of the 2 output signals, which can be important in certain ion trap setups. Without option FL a typical phase accuracy of 1° to 3° is achieved, and typical remaining amplitude asymmetry of 4% to 10%. The latter is limited by unavoidable coupling capacitances, which occur in quadrupole setups or ion traps and which are typically in the order of a few pF. They create a certain amount of unwanted coupling with the two output resonance circuits, thus making it impossible to adjust the amplitudes completely freely and individually. This can be a severe hinderance in cases, where very good symmetry of the two output signals is desired.

The output option FL avoids this problem, at expense of some loss of achievable amount of amplitude (about 20% less). In this case there is only one resonator coil instead of two, representing only one resonance circuit.

The adjustment works as following: Set the output drive level to a certain value (e.g. 50%) and frequency at the desired value. Subsequently the settings of both output trim capacitors are adjusted: their setpoint *difference* defines the matching of output amplitudes, whereas their *combined* setting defines the resonance frequency and thus phase. Ususally one needs to adjust both, frequency/phase and amplitude matching in an iterative way: adjust output symmetry (bringing the number in the display center, above the letters 'A-B' close to zero), then frequency, again output symmerty etc. After approx. 5 to 6 iterations both phase (frequency) and amplitude matchings should be achieved. With this option FL a phase accuracy of 1 to 2° is typically achieved, and remaining amplitude asymmetry of less than 2%, in favor of minimizing micromotion heating effects of stored particles.

Amplitude Stabilization (PID loop)

A special feature of the HF-DR series devices are high precision amplitude regulation loops, which are activated by pressing the corresponding PID/Hold button (press again to turn off again, or controlled via the USB interface). In the instance of activation, the current amplitude reading (A-B) of the differential amplitude between both outputs is captured and internally stored. Subsequently the drive levels of both channels are continuously adjusted in fine steps such that this differential amplitude value is kept constant. As consequence, the amplitude stability is greatly increased in comparison to free running systems. This can be advantageous for precision trapping experiments or experiments related to quantum computing to improve Qubit coherence.

Note that this option comes in two versions, a

- **standard version** with approx. **25 to 30ppm (rms) stability**
- and **enhanced version** with approx. **12 ppm (rms) stability** (option IS-12), which leads to a stability level of about 12ppm over seconds and **3ppm** on a time scale of minutes.

If the regulation loop is capable of maintaining the differential amplitude value, the indicator will be green on the display, red otherwise.

The subsequent graphs depict examples of measured stability at half and full load conditions.

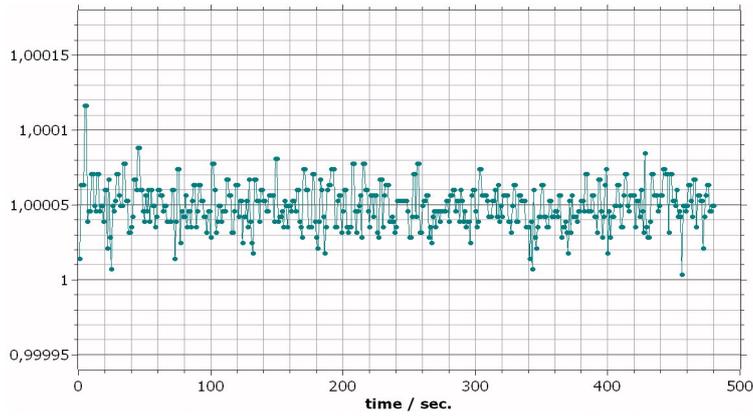


fig. 4a: **Standard version** PID, stability of output amplitude at medium output level (AC voltage difference between both outputs at 500Vpp) after PID is activated. Within a 8-minute interval the instability (relative amplitude deviation) is only on a level of 25ppm rms.

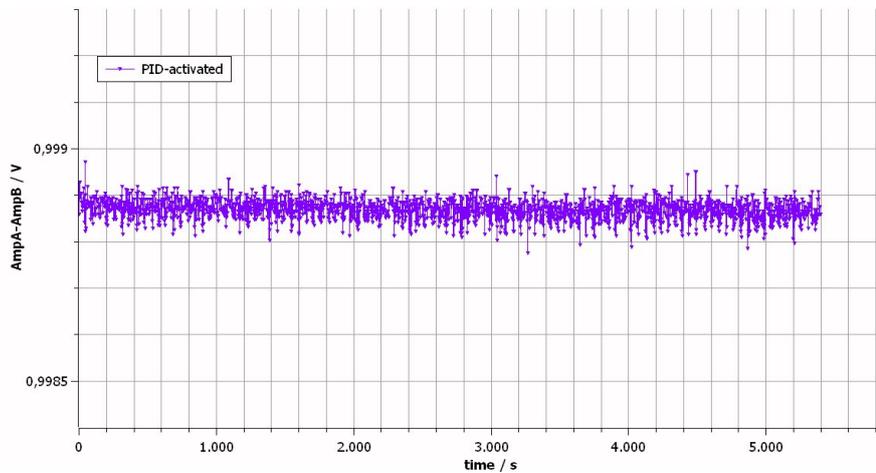


fig. 4b: **Standard version** PID, stability of output amplitude at full load (1800Vpp, approx. 80pF load), PID being activated. Within a 1-hour interval the remaining instability (relative amplitude deviation) is on a level of 30ppm rms with respect to the nominal value.

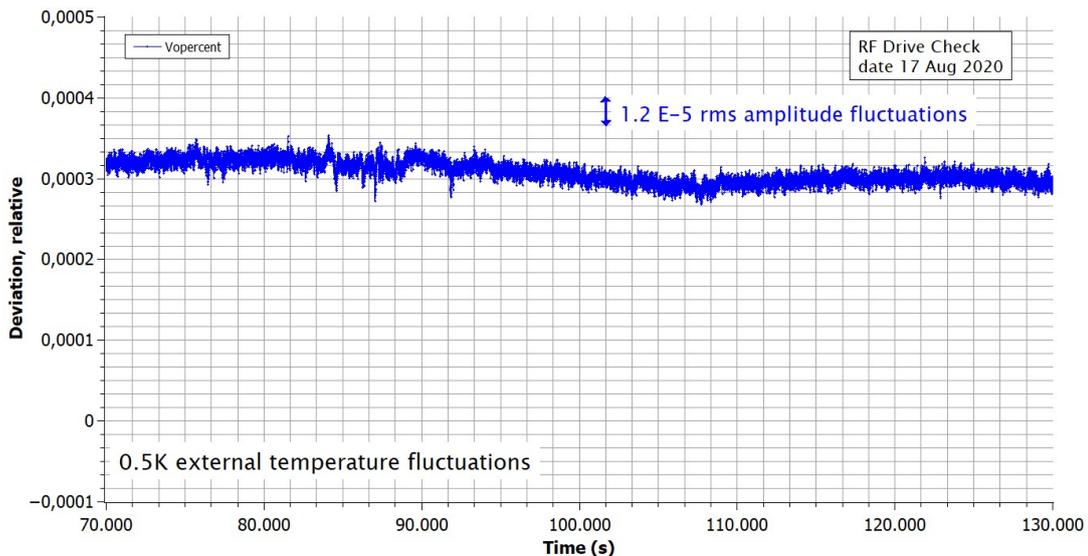


fig. 4c: **Enhanced version** PID (option IS-12, without Option T-PID), stability of output amplitude at full load (1800Vpp, approx. 40pF load), 15 hours depicted. On a time scale of hours the instability (relative amplitude deviation) is on a level of 12ppm rms. Diagrams above (figure a to c) refer to a typical output frequency between 1 and 4 MHz (higher frequencies see fig. 4f).

Start Up Behavior and High-Stability Mode

In general, all kind of amplitude sensors incorporate a certain degree of temperature dependent errors. In PID mode, the HF-DR series devices use highly precise temperature-compensated sensors to measure and stabilize the output amplitude. However, every temperature-compensated sensor still features a small but non-negligible residual error, which comes into play, when observation of amplitude changes on an extreme accuracy level in the ppm range take place. This is visible e.g. after a cold-start, if the PID is being activated after the output amplitude was completely different (or: zero) before. The following graph depicts the output amplitude (about 3.1kVpp), being measured with an external precision voltmeter, after the device has been started. It takes about 600 seconds (10 minutes), until the internal device sensors have stabilized on a deviation less than about 50ppm (i.e. 0.005%).

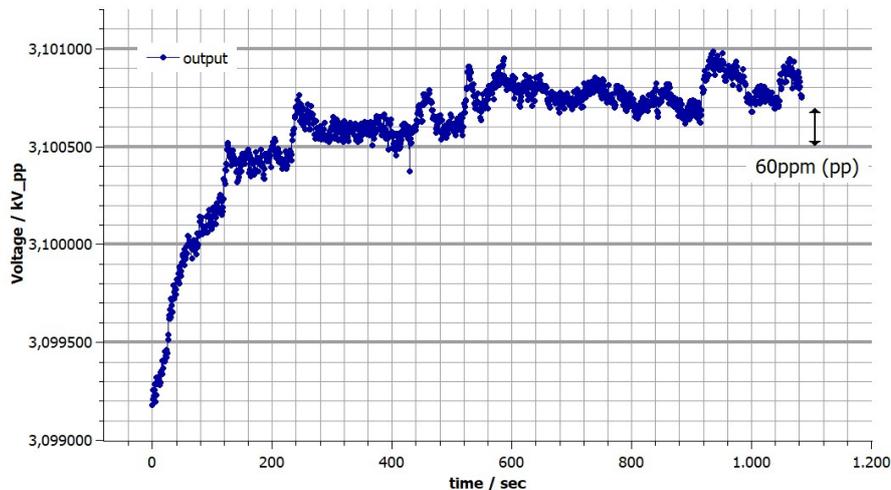


fig. 4d: Startup drift after cold start. After about 10 minutes the initial drift has ceased on a 50ppm level. After 10 minutes, the stability of output amplitude resides on a 60ppm to 70ppm level (about 25ppm in RMS terms) in case of standard version PID. Device: 1MHz, 3000Vpp, approx. 35pF load. As a consequence of these findings, one should allow about 10 to 15 minutes to pass by, after a cold start, or after the amplitude setting has been changed by a large amount, if compliance of the output level on a 50ppm scale is desired.

Stability Histogram

The subsequent graph depicts the probability distribution of output voltage values using 10sec averaging, in terms of deviation from the average of voltage over 1 hour. The RMS value of deviation amounts to about 25ppm rms. Standard version PID mode was used.

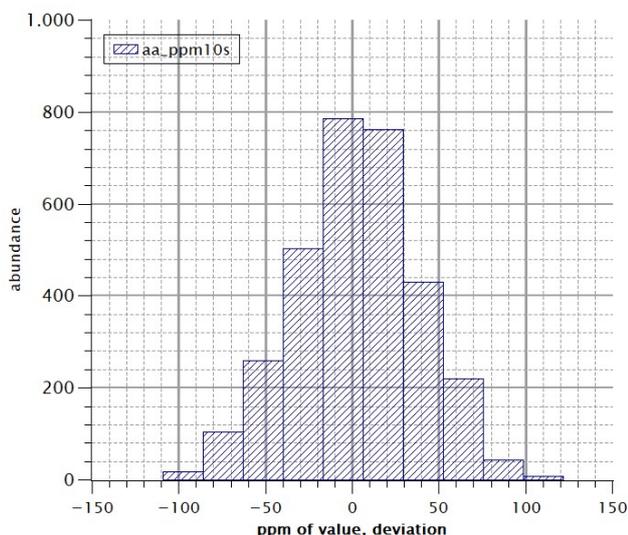


fig. 4e: **Standard version PID**, stability of output amplitude at full load (3000Vpp, 1MHz, approx. 35pF load), PID being activated. Histogram data of deviation from 1 hour average value. Integration time for output voltage measurement is 10sec.

Devices with external Resonator Box

Frequencies above approx. 8 MHz require very close proximity of the RF drive with respect to the electrode structure, to be supplied with the RF signal. To ease this problem, an external resonator case is available, which can be mounted in a convenient short distance (just few cm) to the quadrupole structure or ion trap.



Photo of external resonator box, which can be used to obtain resonance frequencies above 8MHz, e.g. 16 MHz or 20MHz. Apart from the resonator itself, the device may house high precision amplitude detection circuitry and an additional output power stage. The connection to the 19" control unit is established by a 25pole standard Sub-D connector cable. The box can be connected to the respective quadrupole structure or ion trap vacuum flange using a very short flexible (RG174 type) cable with SMA connectors, e.g. of 10cm cable length.

Two additional options are available in this case:

Option IS-12:

This is a combined hardware/firmware option, which helps to stabilize the output amplitude on a level of approx. 12ppm rms, with respect to time intervals of seconds. A PID loop is established for this purpose and high-resolution DACs/ADCs are being employed to counteract amplitude variations with time and temperature.

Option T-PID:

This is a hardware modification, which employs a miniaturized oven, in which the amplitude sensor, which detects the output RF amplitude, is stabilized on a highly stable temperature (few milliKelvin residual variations). This helps to counteract measurement uncertainties, which arise from variations of the ambient temperature in the surrounding laboratory. Thus, the dependence on ambient temperature is reduced from approx. 10 to 20ppm/K down to approx. 3 to 5ppm/K.

Stability Example using a 20 MHz external Resonator Box

The subsequent graph depicts the measured output AC voltage (nominal value 400Vpp) of a 20MHz drive, including options IS-12 and T-PID. The graph shows the measured RF amplitude over a 10-hour interval, using an external separate precision sensor. The 60 second sliding average value are also depicted (red trace), whereas the black trace has been measured taking data points each 500ms. The slight slope (from left to right) stems from a slow 5K temperature variation in the laboratory (corresponding to 5ppm/K residual temperature influence) over 10 hours.

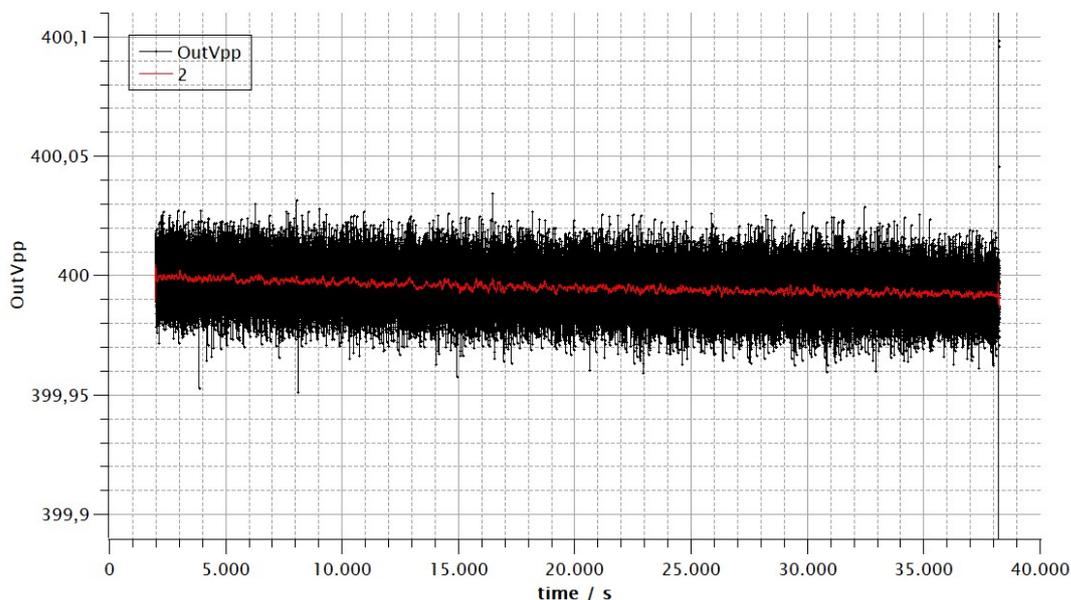


Fig. 4f: Observed output RF amplitude (Volt-peak-peak) versus time, 10 hour-interval; black trace: 500ms sampling time, red trace: 60sec average over black trace

The observed residual amplitude fluctuations at given time intervals amount at $f = 20\text{MHz}$ typically to:

$\Delta T =$	500ms	35 ppm rms
	1 s	12 ppm rms
	2 s	9.9 ppm rms
	10s	5.4 ppm rms
	60s	2.9 ppm rms

These figures are being measured by taking the average value over the specified time interval and subsequently building the difference to the next interval.

Coil Exchange Option (multiple resonators)

This option allows for an exchange of the main resonator coil, to accommodate a certain range of discrete output frequencies, e.g. 1MHz, 2MHz, 4MHz, 8MHz, see also Appendix for further details. This manual exchange is being accomplished by a few manual steps.

Fast Turn-Off Feature (Ion Ejection), Option

Once the output is being deactivated, the ion trapping amplitude decays with a typical time constant of $10\mu\text{s}$ to $20\mu\text{s}$ towards zero amplitude.

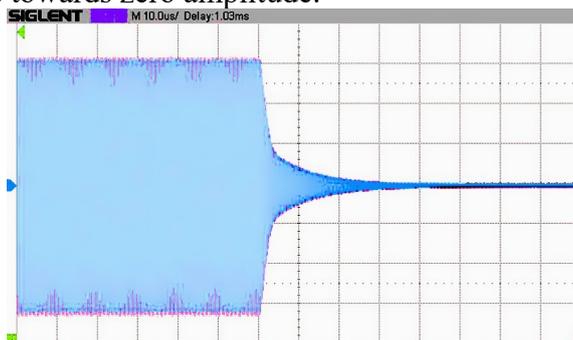


fig. 5a: Typical output waveform decay, after output deactivation (no fast turn off active, standard version), trace with $10\mu\text{s}$ / div. For faster decay, see option below.

In addition, the *fast turn-off* option allows for terminating the output waveform in a much shorter time. This can be useful e.g. for releasing the stored ions out of an Ion Trap in a well-defined way. Upon activation, this circuitry actively damps away the energy being stored in the resonance output circuit, reducing the amplitude within approximately one or two oscillation periods close to zero.

Note that the deactivation of the output signal should not happen at a too frequent repetition rate. A rate of no more than 5 Hz (i.e. cycling the output power 5 times per second) is recommended to maintain a long lifetime of the fast-turn-off switching unit and to avoid overheating (faster rates upon customization). Also note, that fastest damping is reached with highest amplitudes of the device output.

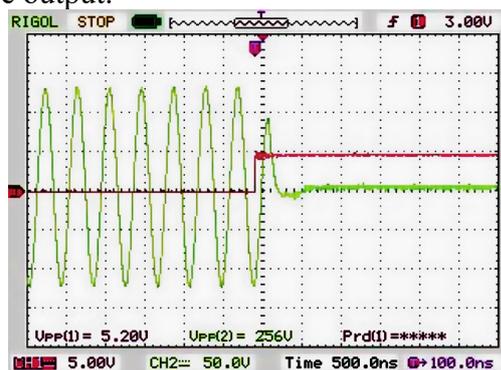


fig. 5b: Experimentally measured waveform during turn-off event, showing typical decay time of 250ns .

If the Fast Turn-Off feature is installed, the HF-DR device is shipped with one of two options installed: synchronized or non-synchronized mode (see also timing diagram in Appendix).

(a) In *non-synchronized* mode (which is the default version) the internal turn-off switch activates about 100ns after the corresponding signal is applied at the respective BNC input, regardless of the current phase of the output oscillation.

(b) In *synchronized* mode the internal turn-off switch is enabled (not yet triggered) after the corresponding signal is applied at the respective BNC input. The circuitry now waits for the

next zero-phase transition of the internal RF oscillator and only then triggers the turn-off switch and damps the oscillation. This special feature allows for precise timing of ion ejection out of traps, being synchronized to the oscillator phase. This creates even better defined ejection conditions than running the system in the non-synchronized mode. Note that, depending on a possible asymmetry of the capacitive output loads, the internal oscillator phase does not necessarily coincide with the phase measured at the output lines, but may have a constant phase offset.

Ramping Option

This option, in case installed, allows to ramp down the output voltage from the current value to a lower one, on a time scale of typically 100 μ s to a few millisecc. This ramping is executed in a time-linear fashion as illustrated in the diagram below (light blue curve) and triggered upon a hardware trigger signal (BNC input on rear side).

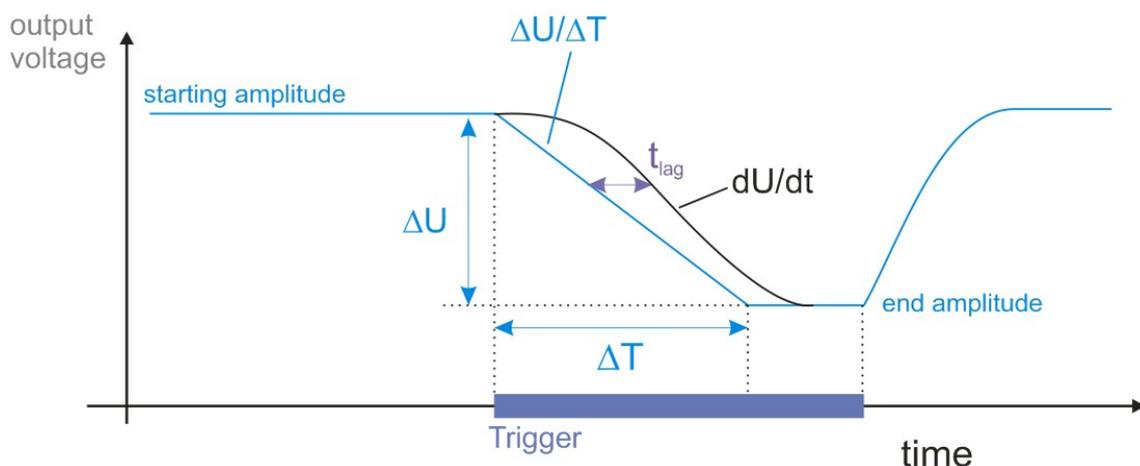


fig. 6: Scheme and definitions of a ramping-down event

In order to prepare the device for executing a ramp, two parameters must be forwarded through the USB connection: The envisaged lower voltage limit 'end amplitude' and the envisaged slope dU/dt . The ramping command has the ASCII format

`PDzzz RAMP yy.yyy xxxx.x`

where `xxxx.x` denotes the 'end amplitude' defined in V_{pp} (please use 4 digits, the decimal point, and the next digit) and `yy.yyy` denotes the 'slope' dU/dt expressed in $V/\mu s$ (please use 2 digits left of the decimal point and 3 decimal digits).

`zzz` corresponds to the device identifier (i.e. the serial number, see programming section of this manual). Please also note the three ASCII 'space' delimiters within the command string (decimal code 32₁₀ each).

The useful range of slope and end amplitude, for which corrections of nonlinear effects are being automatically applied is 1 to 20 $V_{pp} / \mu s$ and 1700 V_{pp} to approx. 50 V_{pp} respectively. The direction of ramp is always *falling*, from higher to lower peak-peak voltages.

Note, that the starting amplitude is *measured* and therefore *defined* right at this moment, at which the command has been sent. Afterwards, every 0 \rightarrow 5V transition at the trigger input will start a new ramp, based on the parameters once received before. The ramps may be repeated arbitrarily often, the device uses the received parameters repeatedly, until a new ramping command will be sent.

After a ramp has been executed, the output RF level will remain on the lower level (i.e. the ‘end amplitude’), until the trigger input returns to the 0V level, thus terminating the ramping mode and letting the amplitude return to the previous level.

Note that there is a slight time lag t_{lag} of the actual waveform, which respect to the ideal one (black curve versus blue curve in the depicted diagram), resulting from the finite Q factor of the high voltage output resonator, which can be seen as a harmonic oscillator with a certain amount of inertia. This delay t_{lag} amounts approximately to $t_{lag} = Q/f$, where f is the resonance frequency and Q the quality factor, being in the range from 50 to 80, typically. For instance, at $f = 1\text{MHz}$ this lag amounts to about $60\mu\text{s}$ and will be much smaller if higher output frequencies are being used.

Also, returning to the previous starting amplitude, once the ramp is being deactivated, (trigger back to 0V) follows this time constant Q/f .

Note that for practical reasons, the external trigger should be stopped, before sending the ramping command and thus new ramping parameters are forwarded. If the ramping command appears accidentally during an ongoing ramp, an error note may be returned to the computer. For the ease of use, the command parameter of the slope, dU/dt , refers to the actual slope and not to the definition of $\Delta U/\Delta t$ (see diagram above). At a frequency of 3MHz, 5MHz or higher, the difference between dU/dt and $\Delta U/\Delta t$ is rather marginal, but matters at lower frequencies.

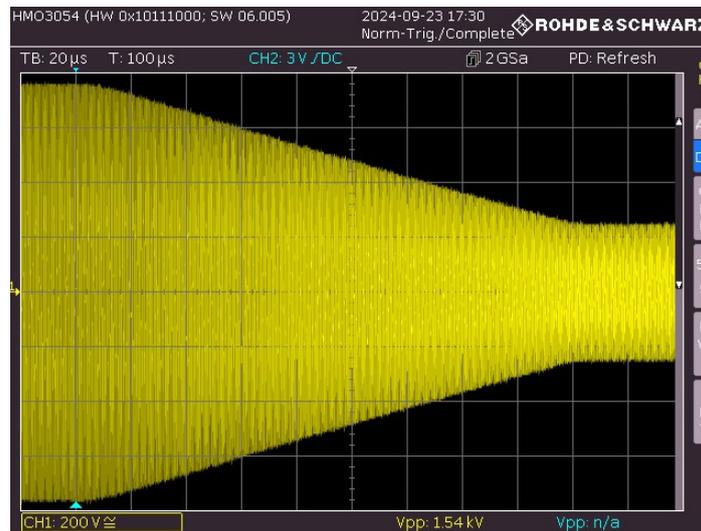


fig. 7: Oscilloscope screen shot of ramping event. The individual periods (5MHz resonator) are not visible due to the order of magnitude of resonator frequency and time base, but the envelope featuring the desired ramp shape is clearly recognizable.

The accuracy of observed ramping behaviour with respect to the envisaged command values is typically in the percent range (actual dU/dt and the end amplitude), please see error graphs in appendix.

Fan/Temperature Warning

In case there is a problem with the ventilation fans or signs of overheating inside the device, the latter is indicated by a display reading. Eventually the output is intentionally disabled for safety reasons.

Output Offset

The output waveform can be superposed with a DC offset, being applied to the rear side of the device. Typical DC offset voltages of $\pm 300\text{V}$ may be applied. Whether one or several outputs are involved, may be customized.

Note that (in case installed) a monitoring output has typically $1\text{M}\Omega$ series resistor for safety reasons, thus if measured with a standard multimeter ($10\text{M}\Omega$ input) the measured voltage is typically 10% smaller than the applies (i.e. voltage divider).

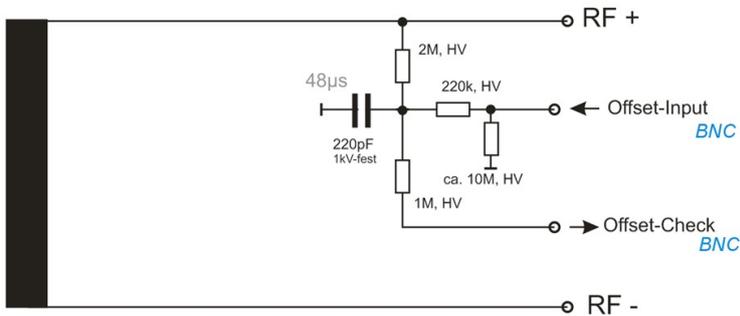


Fig. 8: example of circuitry, featuring a linearly added offset to the output waveform

Typical Output Harmonics

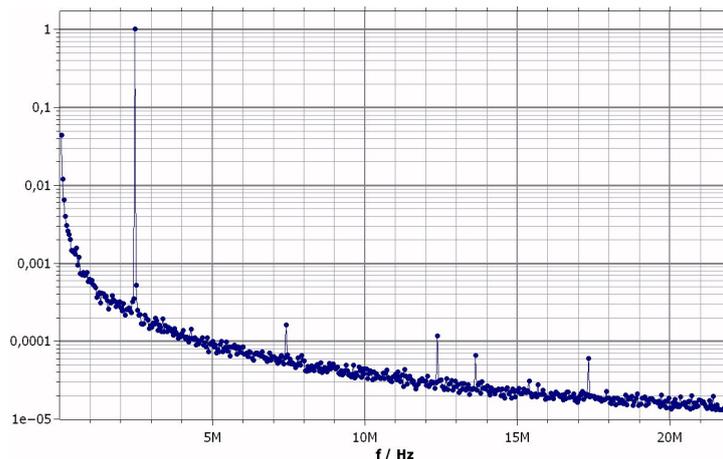


fig. 7: Experimentally measured output spectrum to illustrate the contents of higher harmonics. Fundamental wave frequency used: 2.5MHz at 500Vpp output differential amplitude (device with serial number 016010).

The output waveform is essentially a sine wave with very little remains of higher harmonics. The depicted graph shows a typical output spectrum (measured by a Rigol DSA815 spectrum analyzer), in which the fundamental wave is visible and above the noise background of the analyzer some harmonics at multiples of the fundamental wave frequency appear.

Remote Operation

The device can be remotely controlled by simple ASCII-commands, using the (galvanically isolated) USB connection at the rear side and common high-level languages like LabVIEW, Python, Pascal. By pressing the 'function' button the modes are switched between local and remote. Please refer to appendix below for command syntax. In 'remote mode', manual pressing of the control keys on the front plate is disabled. The device can also be equipped with user-defined commands, please contact manufacturer for this purpose.

Specifications

Output	
Output voltage Option FL	Depending on device version: each output line max 1100Vpp (peak-peak) @ approx. 35pF load after cable; differentially 0 to 2200Vpp ¹⁾ @ approx. 35pF load; other voltages in customized versions. External resonator box: typically 400 to 500Vpp. Note that routinely a safety limit is active, to prevent amplitudes getting too large. This feature sets the drive level to smaller values in case the max amplitude is exceeded.
Frequency accuracy	model depending; fixed center resonance 1.0 MHz to 8 MHz with exchange coils optionally. Versions up to 40MHz upon customization with external resonator box 50ppm standard, 1ppm optional
RF Output Power	typ. 10W, < 20W, steady state value
Amplitude Stability PID activated, standard PID option IS-12	30ppm rms standard $\Delta T = 1s$ to 30s typ 12ppm rms optional (option IS-12) at $\Delta T = 1s$ typ 5.4ppm rms optional (option IS-12) at $\Delta T = 10s$ typ 2.9ppm rms optional (option IS-12) at $\Delta T = 60s$ (contact manufacturer for further details, see also PID section)
Tuning Capacitor Range each Channel (rear plate)	typ. 5pF to 30pF
Parasitic Harmonics Suppression	typ. 75dB (66% to 80% drive level)
Output connector type	SHV, SMA or Amphenol multipin plugs (customizable)
DC Offset (option)	max. +/-200V DC (optionally +/-500V) applied to rear side Sub-D offset input, 25pole male, or BNC input
Capacitive load capability each output	< 50pF recommended Amplitude specs are reached with not more than 35pF each output Please contact manufacturer for higher loads.
Auxiliary Excitation (Option) Input (front plate)	0 to 10Vpp, 50 Ω transfer function: approx. 1/800 of amplitude is superposed to RF amplitude, see also description in text
Trigger Input Ramping (Option) BNC input on rear plate, TTL/CMOS compatible	Low Level: 0V nominally (0V to +1.5V), ramping not active High Level: 5V nominally (+2.5V to +5V), ramping active Transition 0V \rightarrow 5V triggers ramp Input impedance 1k Ω versus GND Trigger delay: typically 200ns, plus resonator response time (lag), which amounts to $t_{lag} = Q/f$, see text.

Ramping Scope	Linear downward slopes of 1Vpp/μs to 20Vpp/μs supported Start amplitudes between 1800Vpp to 500Vpp supported End amplitudes between 1700Vpp to 50Vpp supported For adjustment errors with respect to programmed ramping parameters see Appendix	
Front Plate Display Readings		
Output Voltage Accuracy: (voltage larger 450Vpp, or 33% of nominal max. amplitude, whichever is smaller)	Typical	maximum error
Scale error	2%	4%
Offset error	10V	7V
Output Voltage Difference (floating coil option FL)		
Scale error	2%	4.5%
Offset error	10V	7V
Phase Accuracy (RF voltage larger 125Vpp)	Typical	
Error	1.5°	6°
Environmental Conditions		
Magnetic Field	max. 5mT admissible	
Storage Temperature	-55 °C to +85 °C	
Operating Humidity & Temperature	noncondensing relative humidity up to 95% temperatures between +0°C to +30°C. Note: avoid operating the device in ambient temperature higher 30°C	
Dimensions		
Control Unit, 19"-format Size	19.00" wide x 10" deep, nominally. Front-panel mounting holes are configured for M6 rack configurations	
Weight	approximately 7.2 kg	
External Resonator Box (in case installed) Size	approx. 165mm x 105mm x 88 mm	
Weight	approximately 0.5 kg	
Power Requirement		
AC Supply Rating	EU version AC input voltage 230V _{AC} at 50Hz, typ. 90W at max. RF amplitude US version AC input voltage 120V _{AC} at 50Hz or 60Hz, typ. 90W at max. RF amplitude. Voltage tolerance +5% to -8% The power entry module is EMI/RFI-filtered. Fuse: medium fast blow 1.6A (EU, 230V _{AC}), 2A (US, 115V _{AC})	

Table 2: General device specifications

Note 1) : Capacitive loads must be minimized for achieving highest amplitudes. Note that dissipative effects of loads can significantly reduce maximum amplitudes; this is especially true for high output frequencies above 2MHz. At upper frequency end (towards 8MHz) highest achievable amplitude is typically 450Vpp each output, i.e. 900Vpp differentially

Software installation

USB-Driver

The device uses the USB bus for connecting to a control PC. Windows operating systems are assumed in the subsequent description. After proper cabling of the USB connection (see section before) the “Found New Hardware Wizard” under Windows should open up. Depending on the Windows version allow a few seconds to automatically identify the connected device and install drivers, or follow the described steps below. The installation procedure will install the USB CDM drivers from FTDI, which is the manufacturer of the USB bus interface circuitry.

The supplied installation CD provides suitable drivers for operation under Windows XP or higher. Latest drivers, also for different other operating systems (Linux, Mac OS, other Windows versions) can be downloaded from <http://www.ftdichip.com/FTDrivers.htm>.

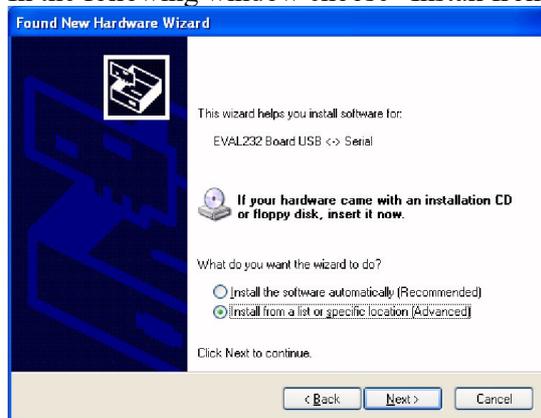
While latest Windows versions like Win10 automatically install suitable drivers (please allow 30seconds to pass once the device has been connected), execute the following steps under Windows XP after automatic start of the “Found New Hardware Wizard”:

The following screen opens up,

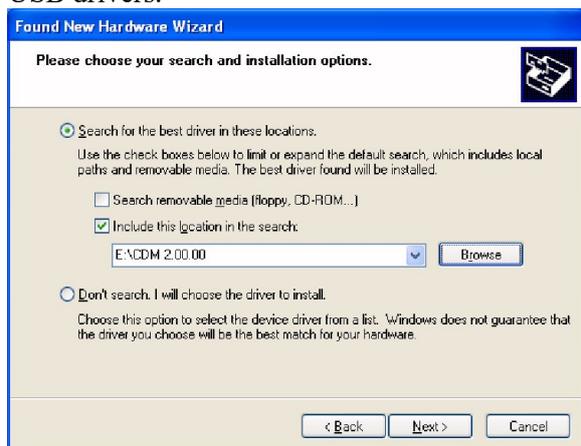


in which you activate the last button “No, not this time” and continue with “Next”.

In the following window choose “Install from a list or specific location” => “Next”



And afterwards you choose “Search for the best driver in these locations” and “Include this location in the search”. Browse now to the Installation CD and select the appropriate path with the USB drivers.



Click “OK” and “Finish” to complete the first driver installation.

After a few seconds the first window will show up again (“Found New Hardware Wizard”). This is because the driver comes in two separate parts, which both have to be installed. Go through the installation steps in the same way as before. After completion, the USB drivers are ready for use and indicate this by showing “HV Series: Device Ready” (or similar) in the lower right screen corner of your PC for a couple of seconds. Depending on operating system (e.g. Windows10™) installation of drivers may happen automatically; this will usually take 20 to 60 seconds and will be indicated by the operation system.

Remark: One known problematic point with National Instruments drivers is the fact that they tend to collide sometimes with other drivers, especially for Tektronix oscilloscopes. If in doubt the other drivers should be temporarily removed and installed again.

LabVIEW™ control program

Assuming that LabVIEW™ in Version 2013 or higher is available on the target PC, copy the path containing the LabVIEW™ source code VI’s from the installation CD or USB-Stick to a place of your choice on a local drive. By double-clicking on the corresponding source file, the control panel for the device will open, which can immediately be put into operation by clicking on the start-arrow in the upper left corner.

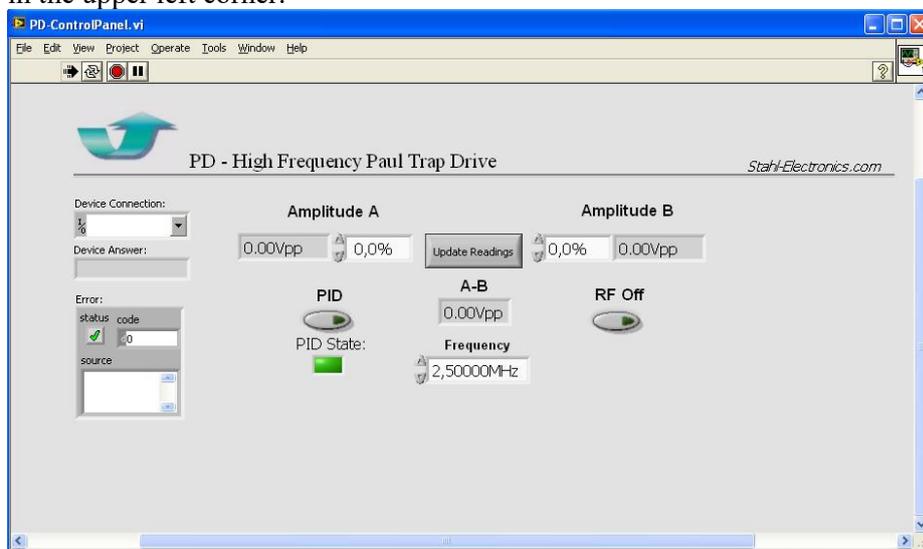


fig. 8: Appearance of LabVIEW user surface

The device parameters may be entered in the corresponding numeric fields, after choosing the established COM-connection to the device and serial number ('PD' + last three digits). Devices with option 'FL' feature only one drive level setting.

Please note, that this LabVIEW program is mainly intended for illustration purposes and in order to get started with the subject of controlling the device. In most applications the device may be controlled later by self-written code or code embedded into an experimental control framework. For this purpose, the required commands and syntax is described below. Please observe that the command set is continuously being improved and the available set of commands relates to the production date of the device (this manual version: Oct. 2021 and later)



fig. 9: Symbolic representation of how to connect the device to a control PC

User-Defined Remote-Control and List of Commands

Introduction

The device can be controlled using the provided LabVIEW™ source code blocks, or by self-written program code. Standard program compilers/interpreters like C++, BASIC or Pascal/Delphi dialects may be used for this purpose and also simple command-line terminal programs (e.g. HyperTerminal™) will do. The physical line connection to the device (USB-connection 1.0 protocol, but also 2.0 compatible) needs to be established beforehand, like described in section 3.2.1. USB-drivers for Windows™ versions, Mac OS and Linux are provided. Please check eventually the USB-manufacturers homepage (www.ftdichip.com) for latest updates.

Note that the physical communication acts like a so-called RS232 device, communicating with standard settings (115200 Baud, 8N1 protocol, no handshake). In self-written code the 115200 Baud rate parameter needs to be set accordingly. In Windows™ operating systems the device appears on a 'COM'-port, as soon as connected to the control PC (after driver installation). The COM-Port number is assigned by Windows upon connecting the device by USB cable and may change from time to time. The COM-Port settings may be checked by the user inside the Windows™ system control panel.

Command List

Inside this table the abbreviation "DDDDD" represents the name of the device including its serial number, e.g. "PD001" means a device with serial number "001". All commands must be terminated with an CR ('carriage-return') symbol "\r" (escape sequence 'r' or 13 in ASCII code or hexadecimal 0x0D). First, after establishing the USB link to the HV device and turning it on, the IDN identifier should be sent in order to retrieve the serial number, since this serial number will be used to address the device correctly. See also examples and more details after the table.

Command Function	ASCII Strings sent to device or received + CR ('carriage-return') at string ends	Observations and comments
Identify device	sent IDN received DDDDD	The device replies with its name, serial number (DDDDD) and further information. See also examples below this table.
Set Frequency	sent DDDDD F XXXXXXXX received ACK (ASCII Acknowledge, 0x06)	The frequency of the output signal is set. XXXXXXXX is a decimal number between 0000000 and 1000000 which represents the Frequency in Hz. Note, device must be in Remote Mode to react.

Set Amplitude	sent DDDDD X.YYY.YYY received ACK (ASCII Acknowledge, 0x06)	The amplitude of Channel X('A' or 'B') is set. <float> is a decimal number between 0 and 100 with up to 5 decimal places which represents the percentage of the maximum voltage of the internal DAC for the selected Channel. For Devices with a single drive level Channel = Channel B. Note that the number decimals depends on the device version and selected PID accuracy level. Note, device must be in Remote Mode to react.
Activate/deactivate software PID loop	sent DDDDD PID OFF or DDDDD PID ON received PID Started or PID Stopped	This command activates/deactivates the PID loop to hold the output amplitude constant
RF ON/OFF	sent DDDDD ENABLE or DDDDD DISABLE received RF Enabled or RF Disabled	ENABLE enables the RF output and DISABLE disables it. Caution: Switching from remote in local mode automatically enables the RF output, if not disabled by the front panel switch or BNC input. Device must be in Remote Mode.
Read Amplitude	sent DDDDD R X received X.XX or XX.XX or XXX.XX or XXXX.XX	Reads the amplitude of channel A, B or the differential amplitude (X = 'A','B' or 'D'). Caution: the return string has no fixed length. For example, an amplitude of 86.29Vpp will return 4 digits and an amplitude of 239.04Vpp will return 5 digits. Device can be in any mode.
Read PID state	sent DDDDD R PID received PID OFF or PID OK or PID OUT OF RANGE	Reads the state of the PID loop. Device can be in any mode.
Read back Frequency	sent DDDDD F? received XXXXXXXX	Reads back the frequency in Hz with leading zeros (8 digits).
Read back set Amplitude	sent DDDDD X? received X.XXXX or XX.XXXX or	Reads back the currently set Amplitude of A or B (X='A' or X='B') in percent of the maximum Voltage of the internal DAC. Caution: the return string has no fixed length.

	XXX.XXXX	
Device in Local Mode	sent DDDDD L? received 0 or 1	Checks if device is in Local Mode. 0: Remote Control 1: Local Mode
Set up ramping parameter	sent DDDDD RAMP YY.YYY XXXX.X received ACK (ASCII Acknowledge, 0x06)	Sets ramping parameters and expects a ramping trigger xxxx.x denotes the 'end amplitude' in Vpp (4 digits, decimal point, and the next digit) yy.yyy denotes the 'slope' dU/dt expressed in Vp/μs (2 digits, decimal point, 3 decimals)

Communication Speed

The device has a nominal USB-transmission speed of 115200 Baud (115200 Raw-Bits per second). Note that this speed grade corresponds to the 'Fast-Mode' regarding the HV- or BS-Series devices from Stahl-Electronics. However, the cycle time as illustrated below can be up to 25ms. In self-written program allow sufficient time to pass to avoid data collision and 'jamming' of the serial connection, e.g. by waiting for each device answer before sending the next command.

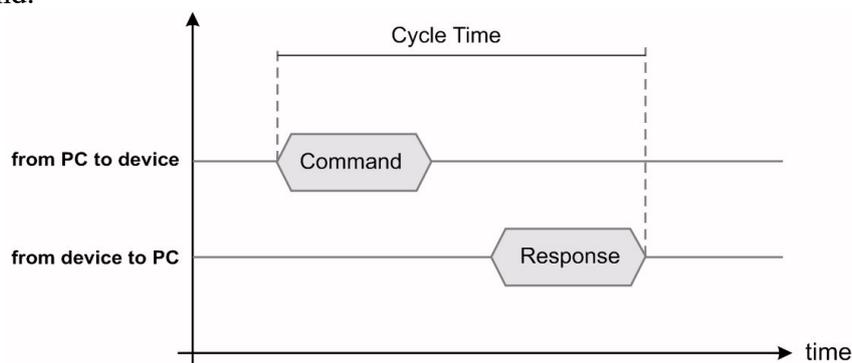


fig. 10: Definition of cycle time, i.e. the time period required to complete a valid

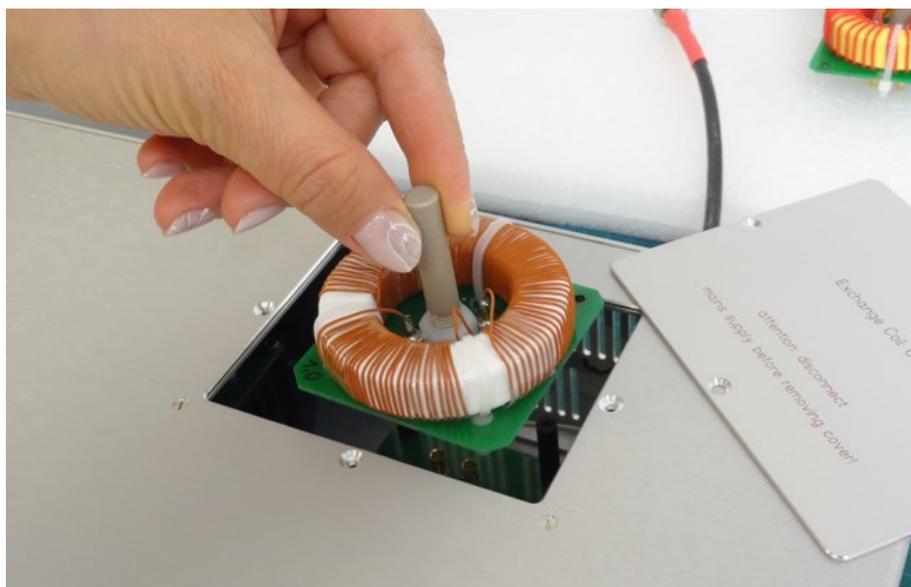


fig. 11 e: Detailed view on exchange coil being removed

Gently pull the mounted resonator coil out vertically by utilizing the grey handle and replace it by the desired new coil. Attention: Observe the **pin orientation** on the bottom side when inserting. Carefully take note of the end stop, when lowering the coil downwards into the device. During the course of pulling out and inserting, please avoid tilting the coil assembly, as this may bend the contact pins. Move the assembly only in vertical direction. Subsequently mount the device cover into its former place.

Table of optional coil set (typical values):

Center frequency (nominal)	Coil inductance (secondary winding)	Maximum operating voltage (Option FL), if permanently operated, differentially between the two outputs
1 MHz	440 μ H	2000 Vpp
1.5 MHz	195 μ H	2400 Vpp
2 MHz	84 μ H	2400 Vpp
3 MHz	53.5 μ H	2400 Vpp
4 MHz	25.3 μ H	2200 Vpp
5 MHz	17.1 μ H	2000 Vpp
8 MHz	6.6 μ H	900 Vpp

Note, that higher resonator frequencies, like 16, 20 or 40 MHz are implemented in external resonator casings, not internal ones as described above. Please contact manufacturer in this case for further details. Note also that the standard exchangeable coil set refers to output voltages of max. 2400Vpp. For larger values please contact manufacturer.

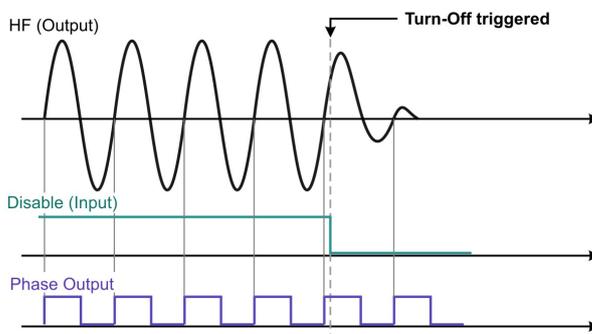
Fast Turn-Off Modes, Timing

If the Fast Turn-Off feature is installed, the device can extenuate the output oscillation within short time. The HF-DR device is shipped with one of two hardware options installed: synchronized or non-synchronized mode. A special version is also available, which allows for selecting non-synchronized/synchronized mode by means of a switch on the front plate. Otherwise, the non-synchronized one is the default version if no other version is chosen at time of ordering the device.

(a) In *non-synchronized* mode the internal turn-off switch triggers shortly after the corresponding signal is applied at the BNC disable input (low level → turn amplitude off), regardless of the current phase of the output oscillation. This phase of the output oscillation can be monitored at the ‘Phase Output’ terminal on the front plate.

(b) In *synchronized* mode the internal turn-off switch is enabled (not yet triggered) after the corresponding signal (low level) at the BNC input and finally triggers after a waiting time T_w at the next occurring zero-phase transition.

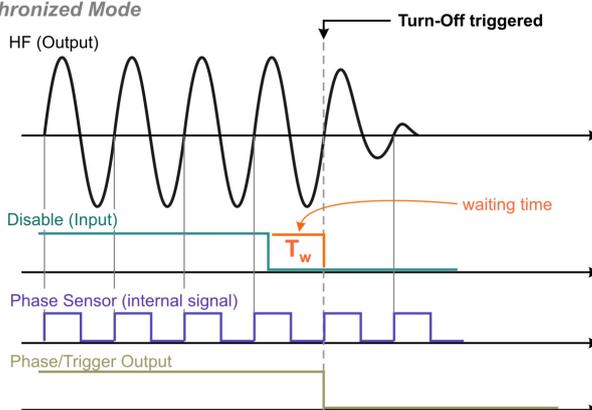
Non-Synchronized Mode



In this case the BNC-‘Phase Output’ shows (with a high-low transition) the time, when the turn-off switch is finally triggered.

Optionally, it is possible to equip the device with a switch to select either synchronized or non-synchronized operation.

Synchronized Mode

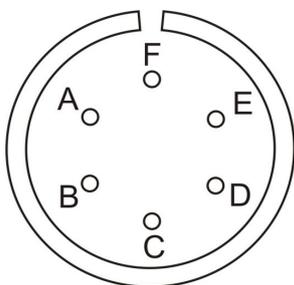


Timing schemes, depicting non-synchronized and synchronized fast turn-off features

Optional Amphenol Multipin Plugs

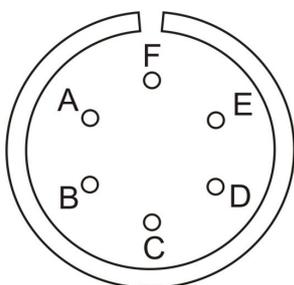
Pin assignment device with Amphenol Multipin Plugs
(e.g. serial number 016010)

Output A:



- A: 1
- B: 2
- C: 3 + Excitation line
- D: 4
- E: 5
- F: 6

Output B:



- A: 7
- B: 8
- C: 9
- D: 10
- E: 11
- F: 12

Connection cable to Bias-Supply (ser. nr. 0170092), SubD- 25pole:

Pin numbers correspond to DC channel numbers 1 to 12; GND is provided by the cable shield mesh.

Option Multipole AC/DC Combiner for Ion Quadrupoles

This option allows to superpose the RF trapping voltage with DC voltage for (static) ion trapping, and also allows for pulsing of elements. Its output connector (e.g. Vacom PLUG-MPC2-32-P-CLG) is meant to connect to a mating vacuum feedthrough flange connector.

The photo below depicts such a unit. One can see the round-shaped connector to a vacuum flange and two more cables entering at the left side, the black carries the RF, the grey the DC voltages. The DC voltages are fed through a standard 25-pole SubD (male) plug.



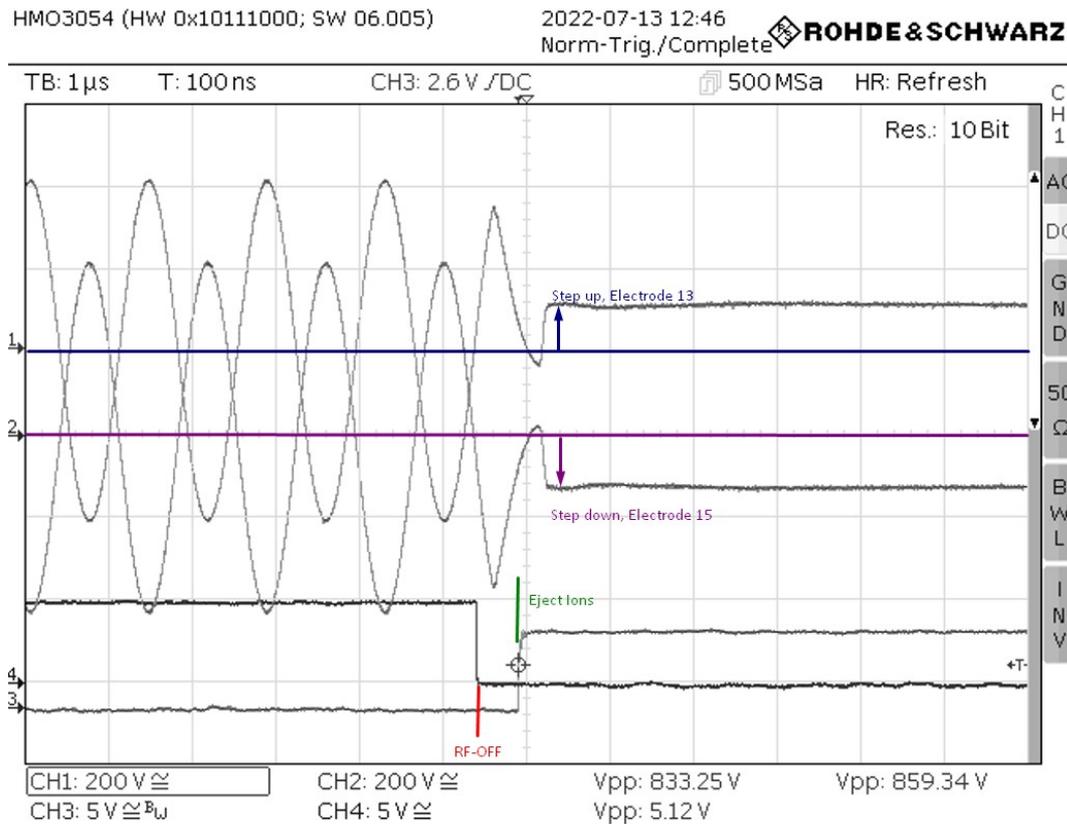
The pin assignment is (unless customized)

SubD	01	02	03	04	05	06	07	08	09	10	11	12	13	14	15
------	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----

Flange	A	B	C	D	E	F	G	H	J	K	L	M	N	P	R
--------	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

Optionally, also pulses may be added, to ease ejection of ions out of a linear quadrupole structure. In this case, the voltages on the output pins are a linear superposition of 3 parts, the corresponding DC voltage, AC (RF) voltage and a pulse voltage. The pulses are applied to additional BNC input. Note *either* positive *or* negative pulses must be applied, depending on the chosen polarity.

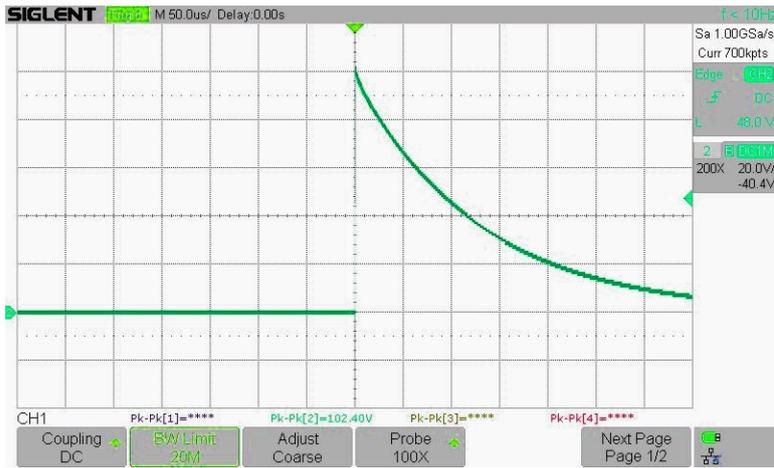
The subsequent picture shows the screenshot of voltages at ion ejection event. One can see the RF on two electrodes (about 850Vpp each), their decrease towards zero after an RF-off-trigger (this assume that the FSO option is installed, see page 13), and two pulses being added to the depicted voltages (+100V on the upper track, -100V on the lower track).



Screenshot of voltages at ion ejection event. Upper voltage traces at 200V/div, horizontally 1µs/ div.

Please note that pulses must only be added if the RF has been **turned off**, at least 500ns earlier, to avoid overheating of the pulse circuitry.

After application of pulses, they *decay* again, on a longer time scale, as shown in the screenshot below (50µs/div in contrast to 1µ/div as above). This means that after about 1milliseconds, the circuitry has returned to the initial state and is ready for the next pulse.

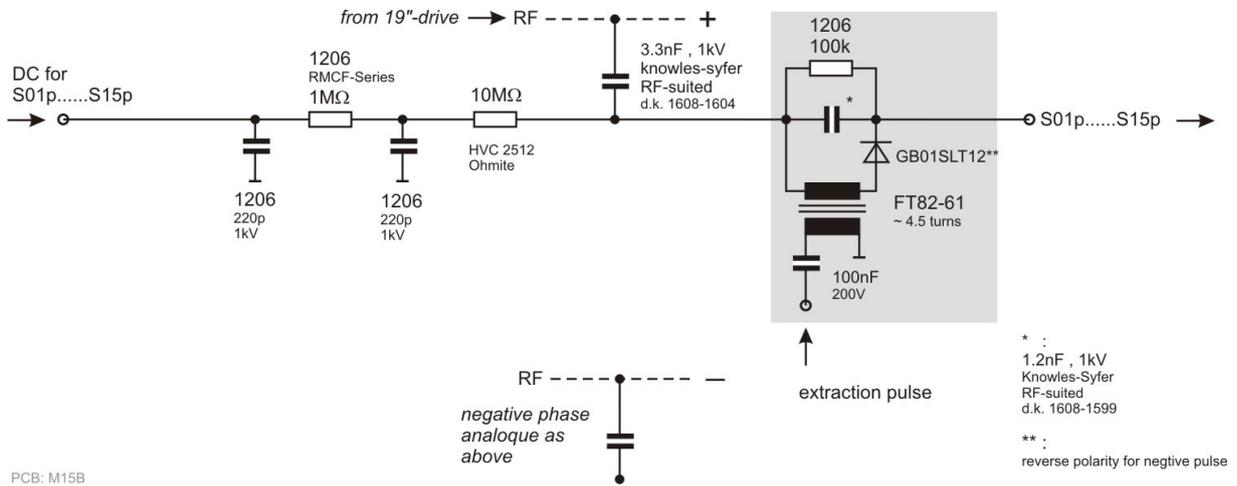


Decay of added pulses on a time scale of hundreds of Microseconds.

Maximum ratings of AC/DC Combiner:

- DC voltages versus GND max. +/-400V
- DC voltages Pin versus any other Pin max. +/-300V
- AC voltage (RF) max. 3MHz, max. 450Vpk (=900Vpp)
- Pulses (option) max either +200V , or -200V, depending on chosen polarity

Circuitry of AC/DC Combiner



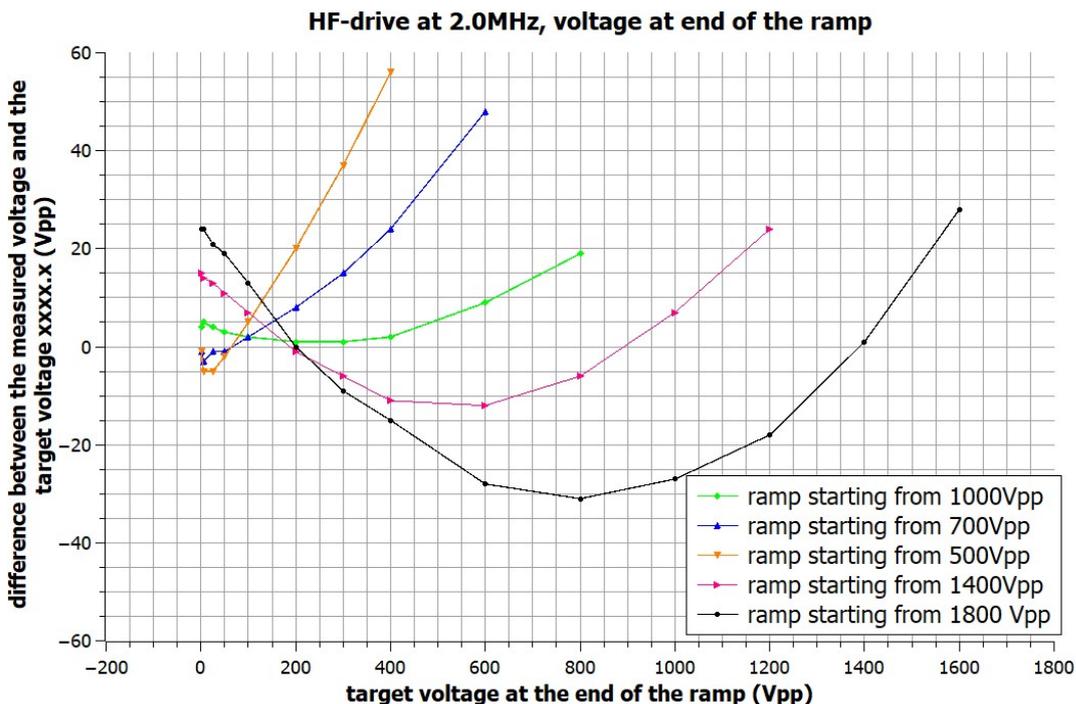
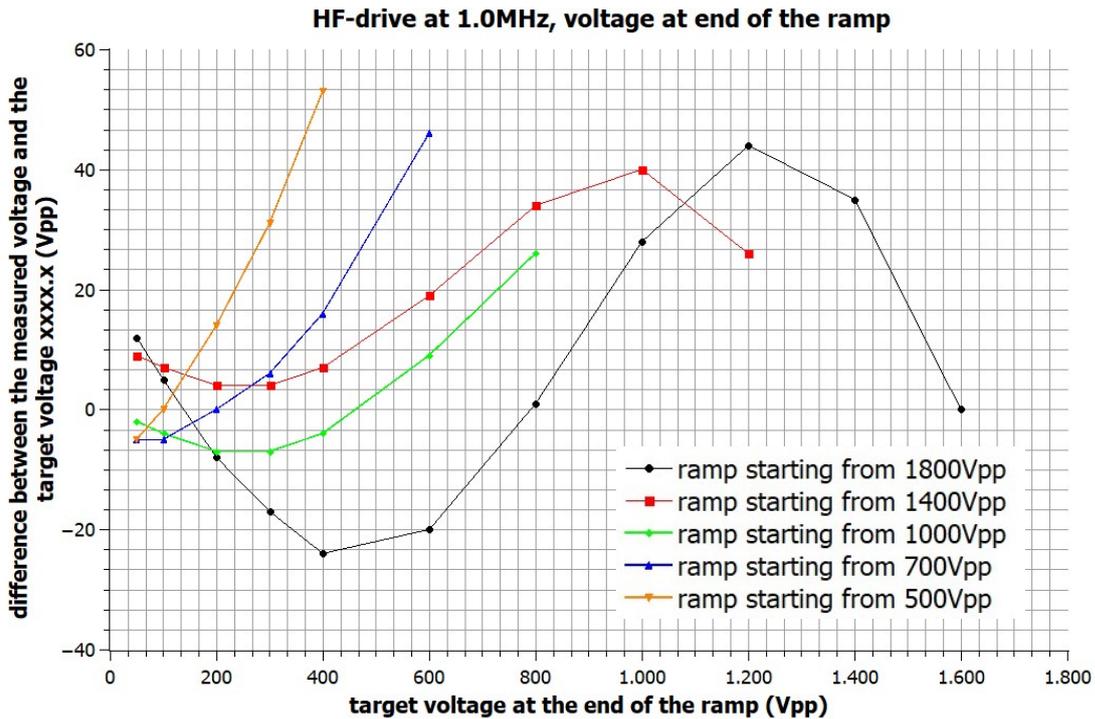
Circuitry of AC/DC Combiner. The grey shaded area applies only if the pulsing option is implemented. The diode orientation relates to positive pulses, for negative pulses the direction is reversed

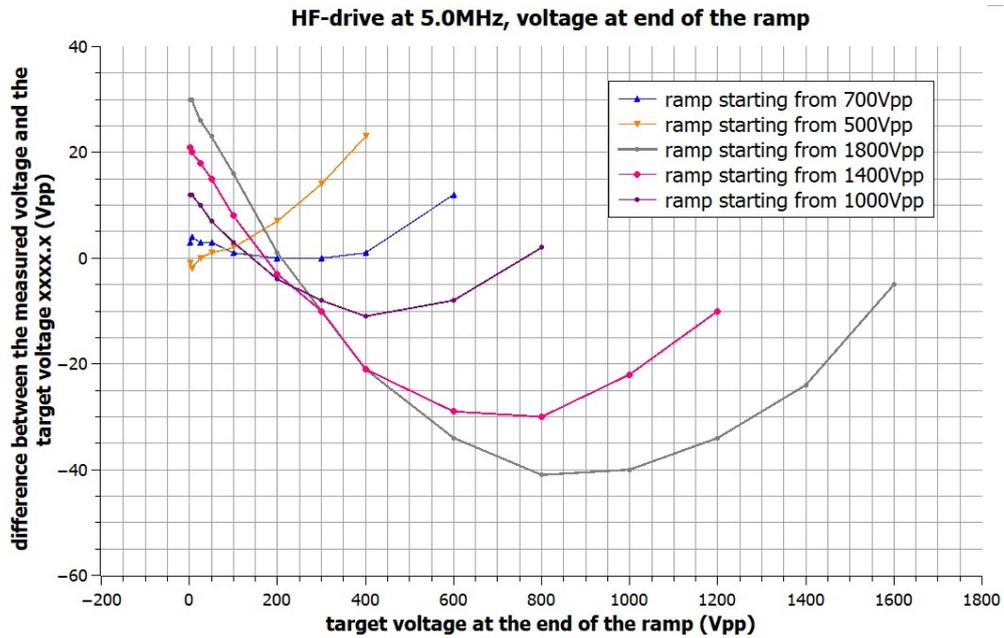
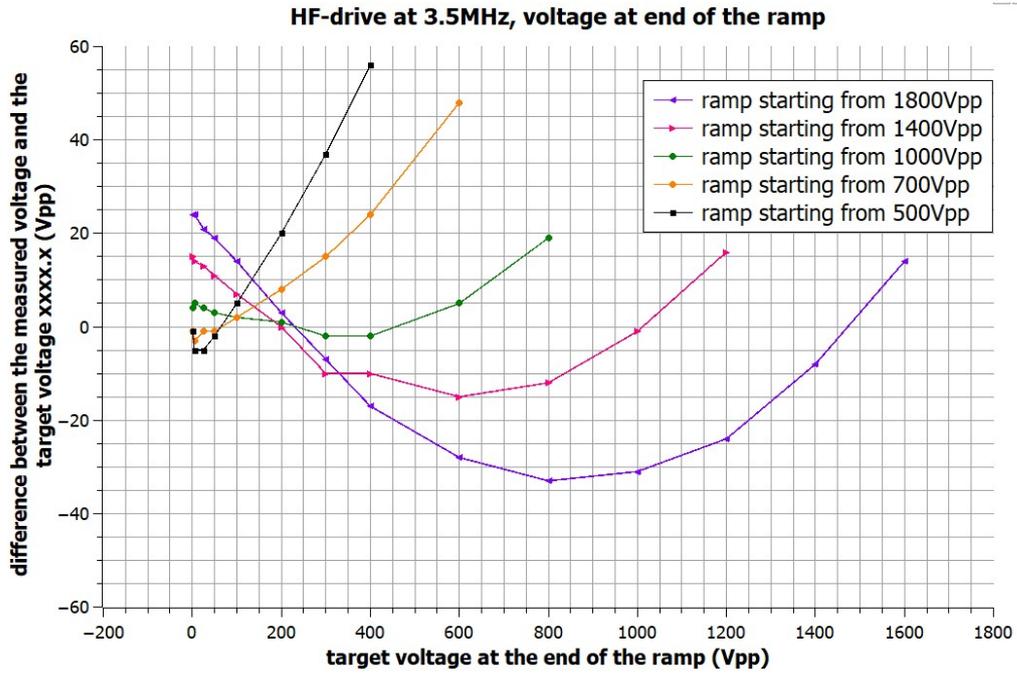
Ramping Option Error Graphs

A ramping option is available, which allows for running amplitude ramps of the output signal (see text above). The subsequent pages depict typical error of programmed set parameters.

A) Error of 'end amplitude'

The subsequent graphs depict typical observed deviations of the measured value of the voltage at the end of the ramp, with respect to the intended target value, for four different output resonator coils.

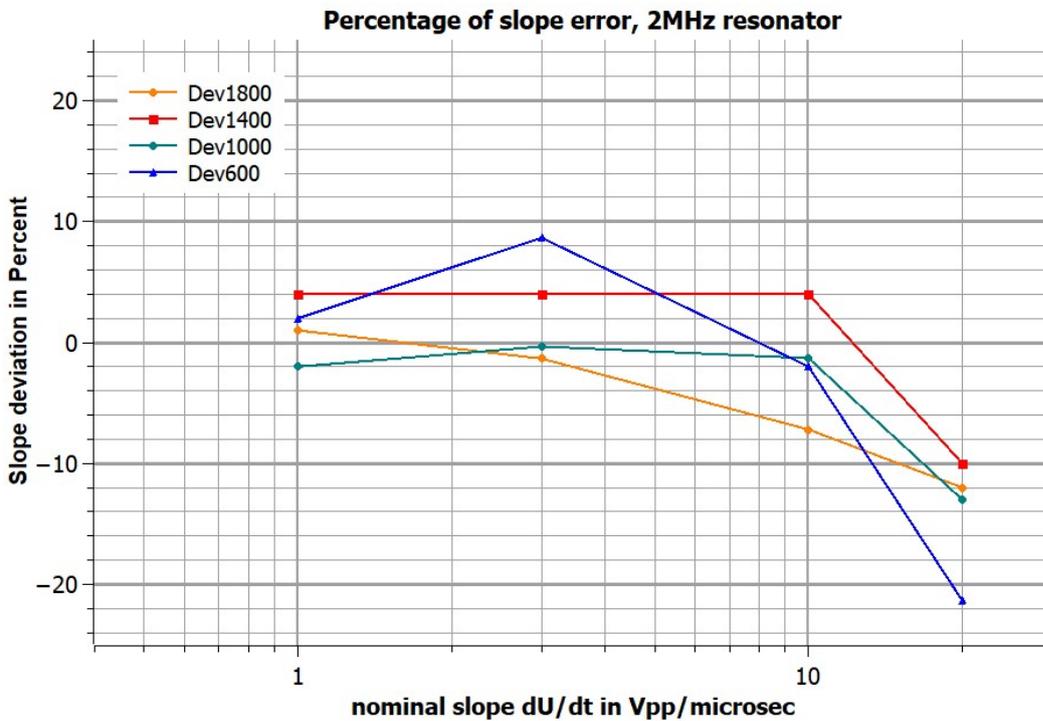
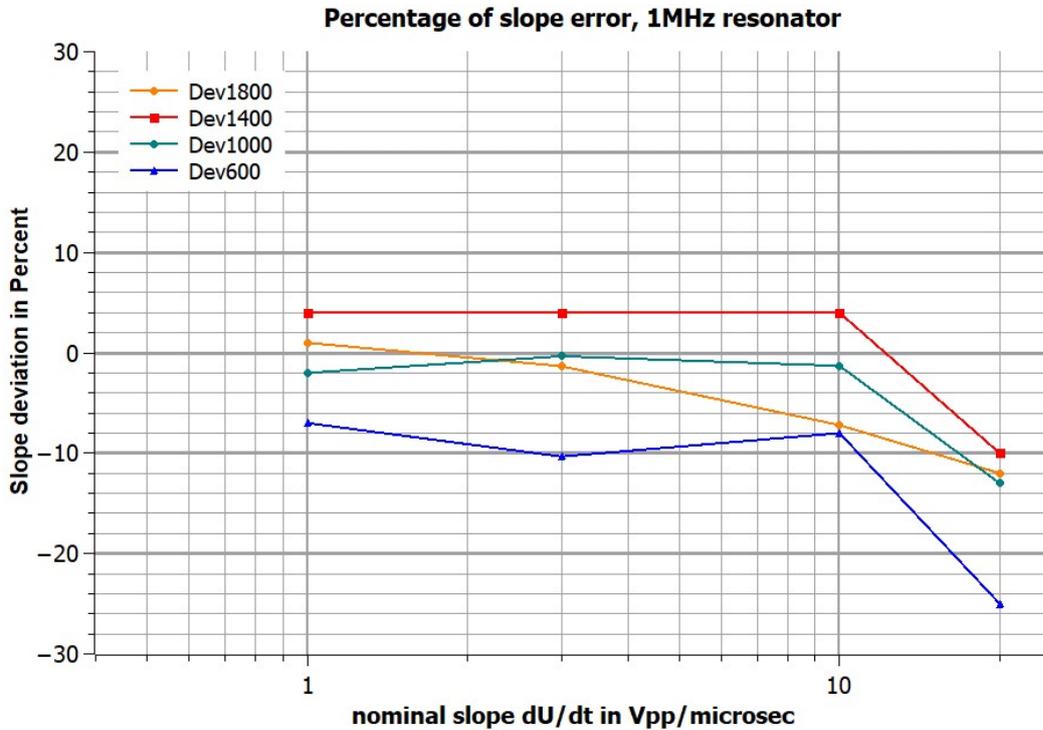


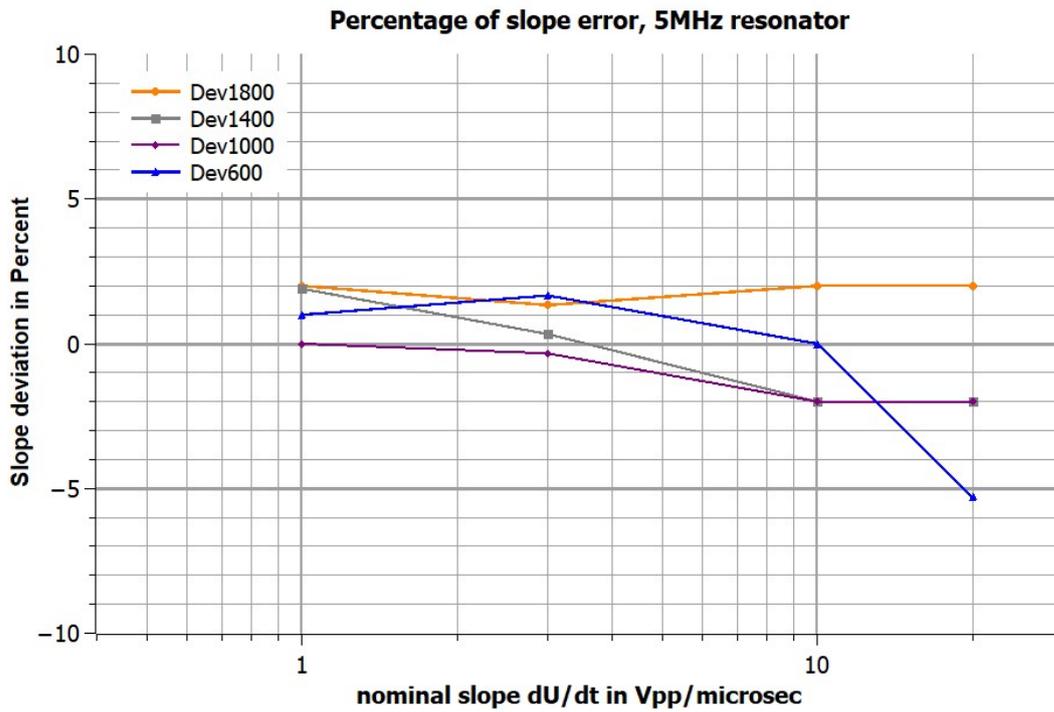
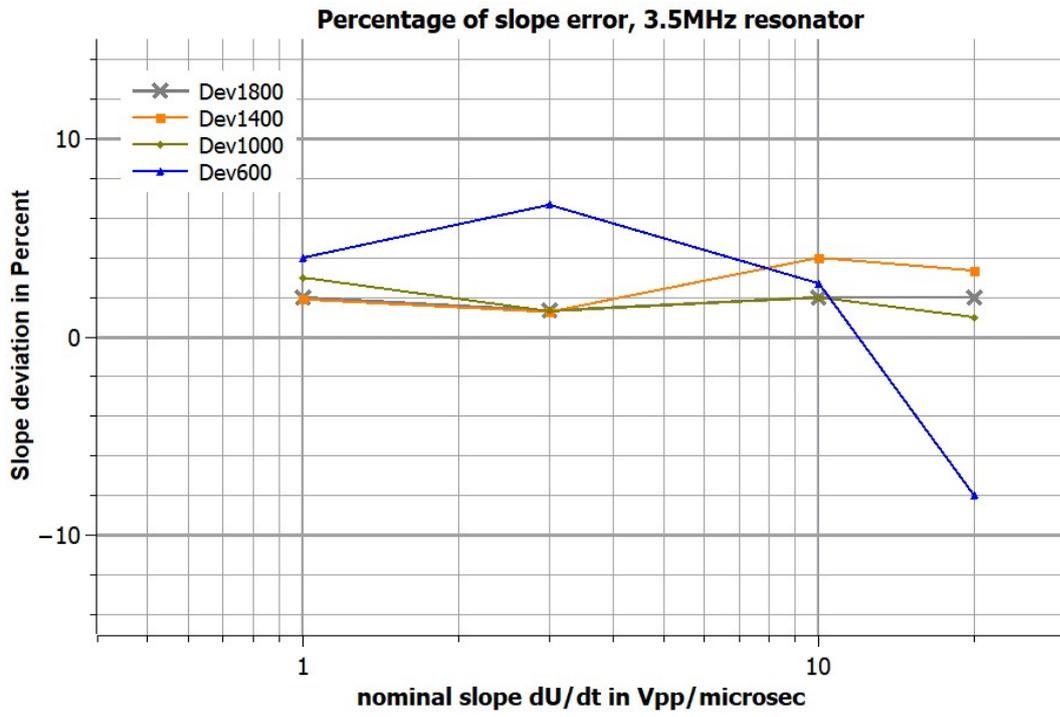


B) Error of ramping slope

The subsequent graphs depict typical observed deviations of the measured value of the ramping slope dU/dt with respect to the intended nominal slope value, for different resonator coils. The deviation (error) values on left axis are in percent.

Parameter is the starting voltage of the ramp, i.e. 1800Vpp, 1400Vpp, 1000Vpp, 600Vpp.





DECLARATION OF CONFORMITY

Manufacturer's Name: Dr. Stefan Stahl
- Electronics for Science and Research -

Manufacturer's Address: Kellerweg 23
67582 Mettenheim
Germany.

Declares, that the product

Product Name: HF-Drive (Paul Traps)
Model Number: HF-DR

Product Options: This declaration covers all options of the above product(s)

Conforms with the following European Directives:

The product herewith complies with the requirements of the:

- 1. Low Voltage Directive 73/73EEC;**
- 2. EMC Directive 89/336/EEC (including 93/68/EEC) and carries the CE Marking accordingly**

Date Of Issue

21. Jan 2017

General Director